

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: T. MATSUURA et al  
Serial, No. 10/108,439  
Filed: March 29, 2002

Group Art Unit: 2818  
Examiner: D. Nhu  
For: A SEMICONDUCTOR DEVICE AND A METHOD  
OF MANUFACTURING THE SAME

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Mail Stop Patent Application  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/108,439.

The Applicants request the Examiner initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

*for Gene W Stockman RN21,021*  
Shrinath Malur  
Registration No. 34,663  
Attorney of Record

MATTINGLY, STANGER & MALUR, P.C.  
1800 Diagonal Road, Suite 370  
Alexandria, Virginia 22314  
Telephone: (703) 684-1120  
Facsimile: (703) 684-1157  
Date: September 25, 2003

FORM PTO-1449  
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
H-957-04

SERIAL NO.

**LIST OF DOCUMENTS CITED BY APPLICANT**  
(Use several sheets if necessary)APPLICANT  
T. MATSUURA et alFILING DATE  
September 25, 2003

GROUP

**U.S. PATENT DOCUMENTS**

* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)
	AA	5,026,669	06/25/91	Shinohara			
	AB	5,327,104	07/05/94	Kikushima			
	AC	5,731,231	03/24/98	Miyajima			
	AD	5,973,395	10/26/99	Suzuki et al			
	AE	6,208,020	03/27/01	Minamio et al			
	AF	6,399,423	06/2002	Matsuura et al			
	AG	6,208,020	03/2001	Minamio et al			
	AH						
	AI						
	AJ						
	AK						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATIO	
							YES	NO
	AL	11-195743	07/21/99	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AM	2-240940	09/25/90	Japan			<input type="checkbox"/>	<input type="checkbox"/>
	AN						<input type="checkbox"/>	<input type="checkbox"/>
	AO						<input type="checkbox"/>	<input type="checkbox"/>
	AP						<input type="checkbox"/>	<input type="checkbox"/>

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	"Semiconductor Assembling/Testing Technique of '99", Extra Issue of Monthly Semiconductor World, July 27, 1998.
	AS	
	AT	

EXAMINER

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form next communication to applicant.